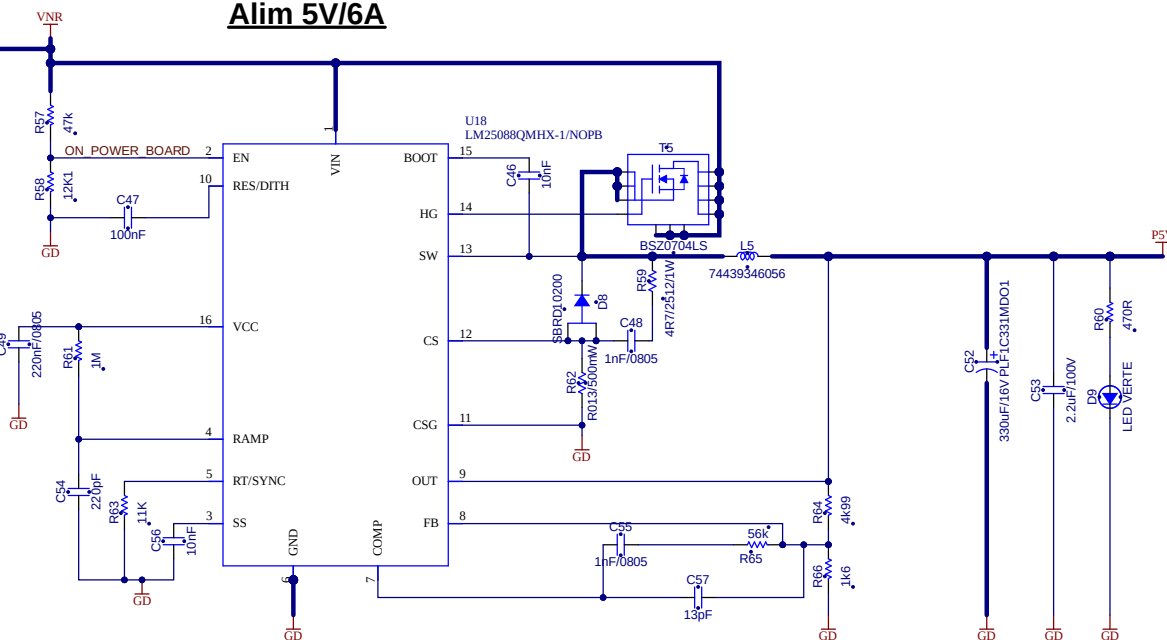
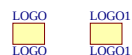
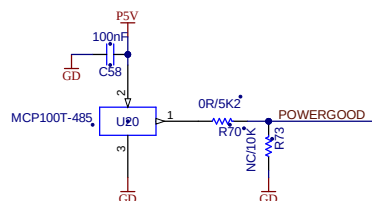
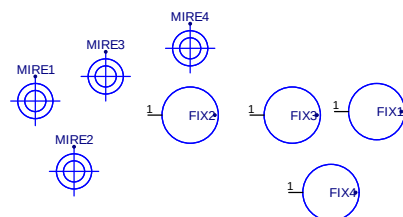
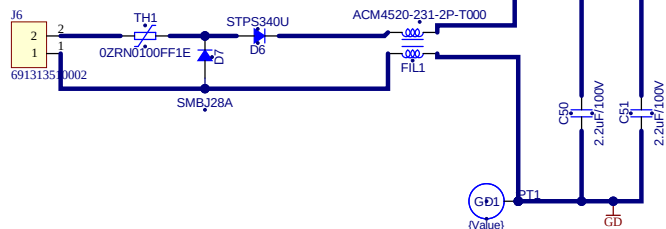


Démarrage de l'alimentation CM4 (5V) par signal ON_POWER_BOARD, issu du microcontrôleur.

A

B

C

D

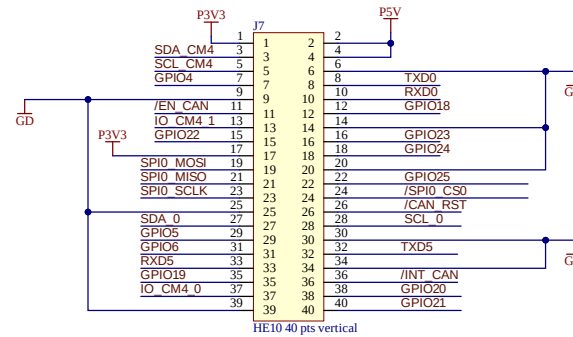
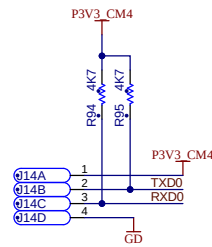
A

B

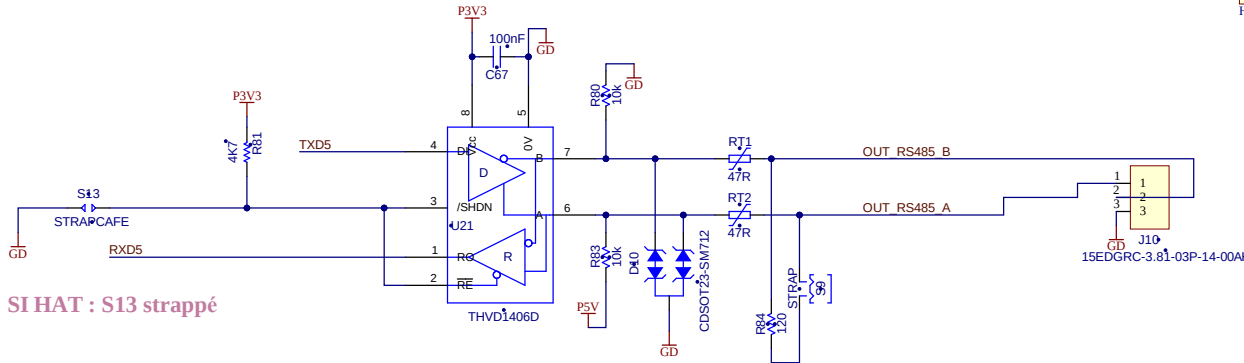
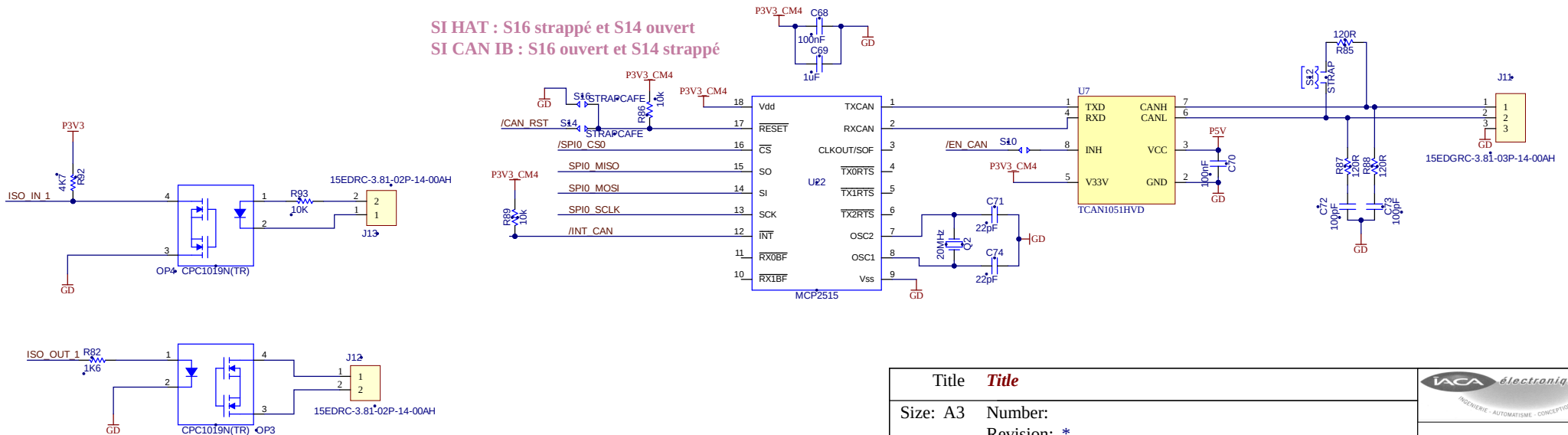
C

D

DEBUG CM4



HE10 40 pts vertical

SI HAT : S16 strappé et S14 ouvert
SI CAN IB : S16 ouvert et S14 strappéTitle *Title*

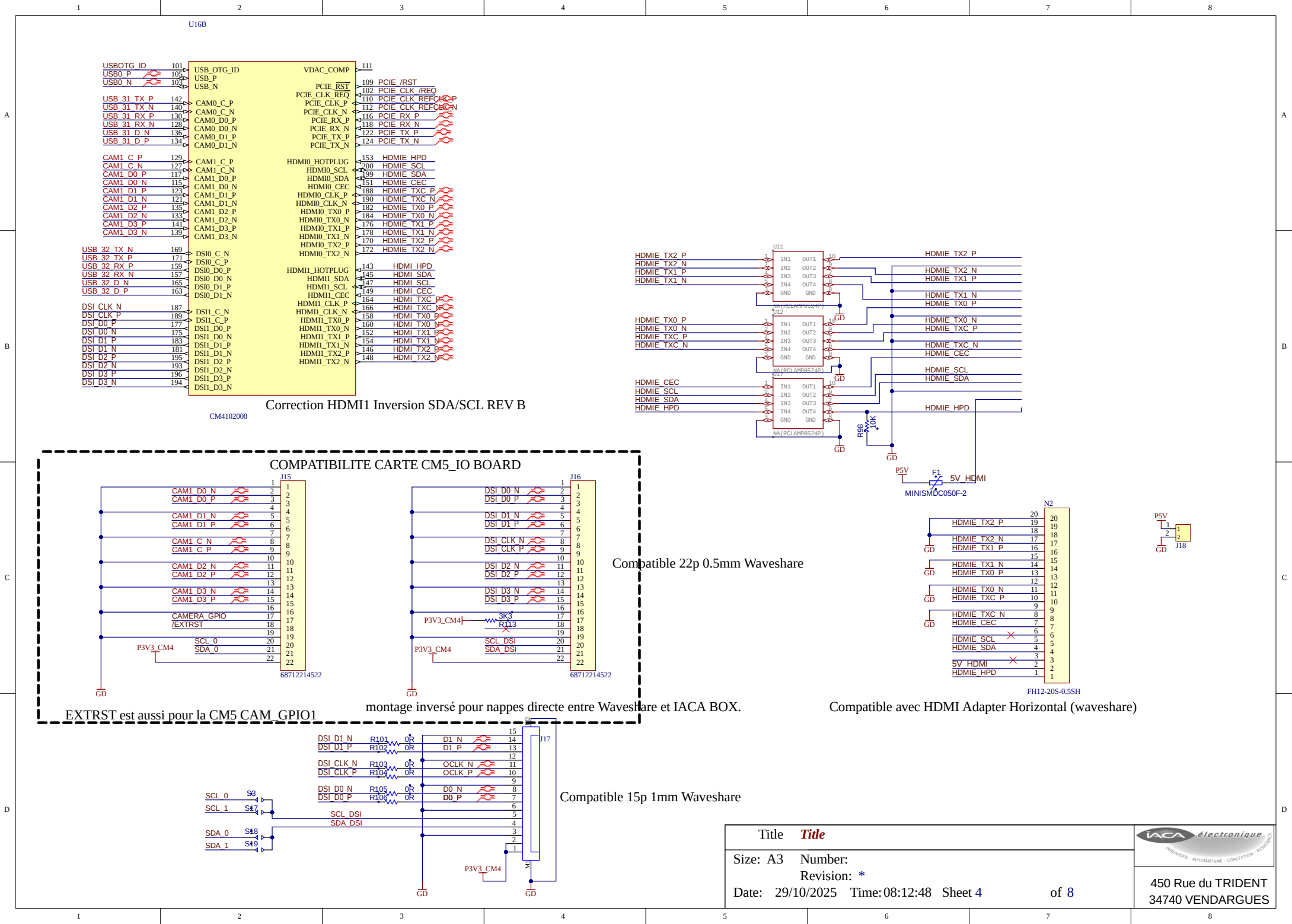
Size: A3 Number:

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34740 VENDARGUES



Correction HDMI1 Inversion SDA/SCL REV B

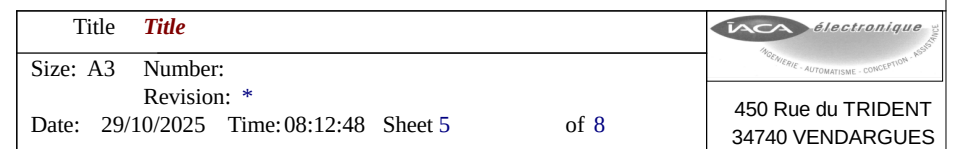
CM4102008

COMPATIBILITE CARTE CM5_IO BOARD

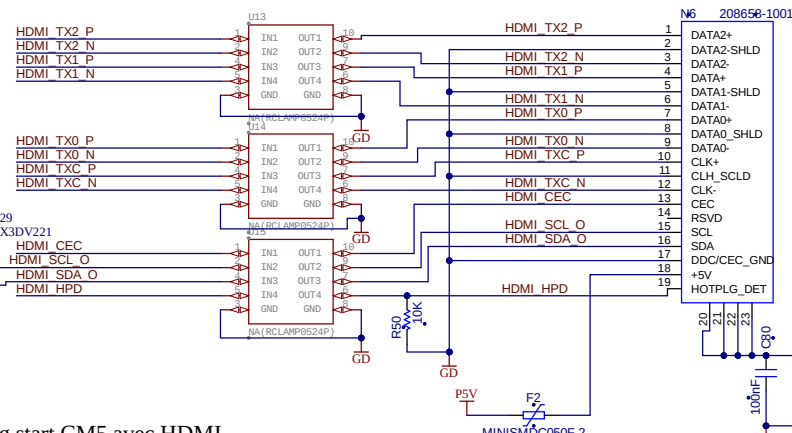
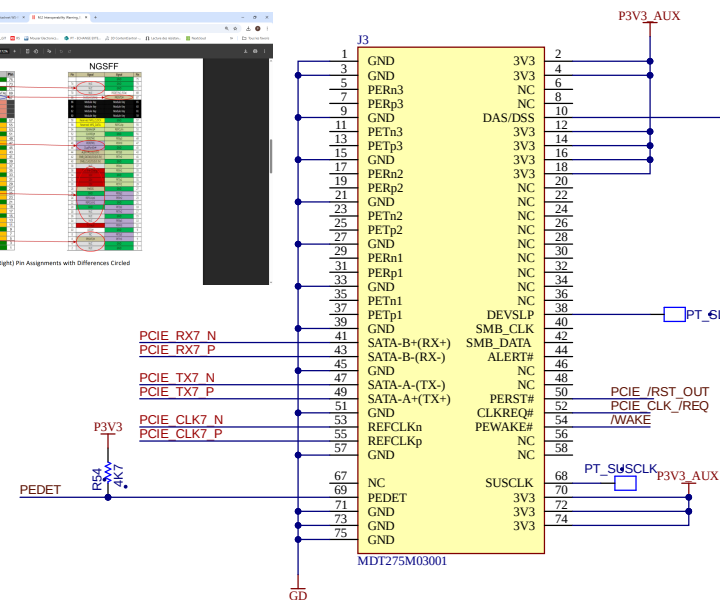
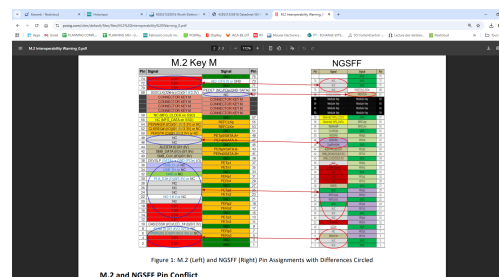
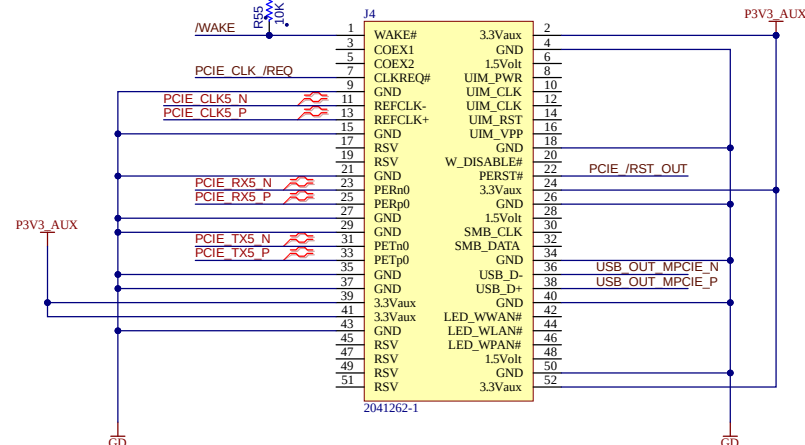
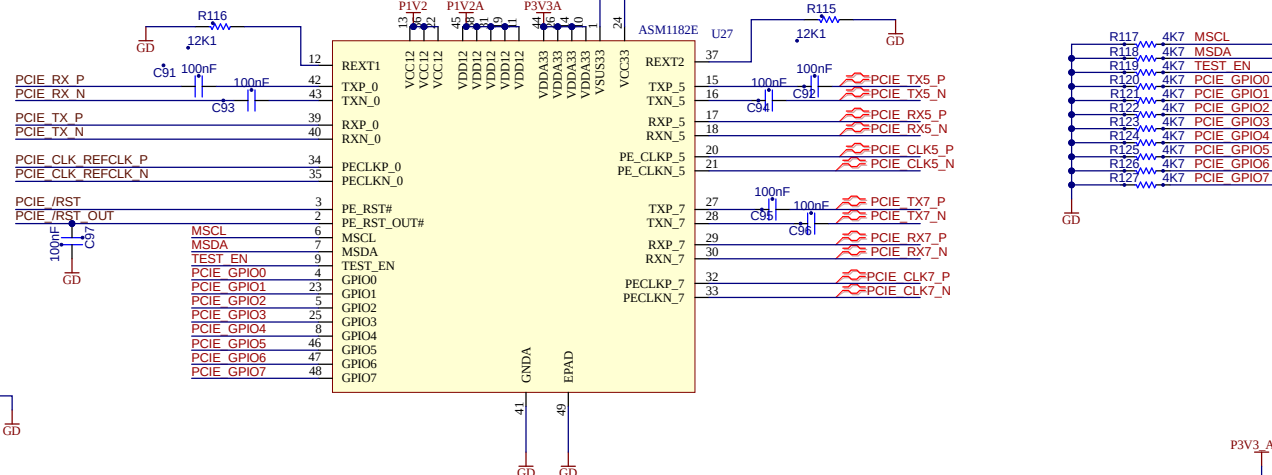
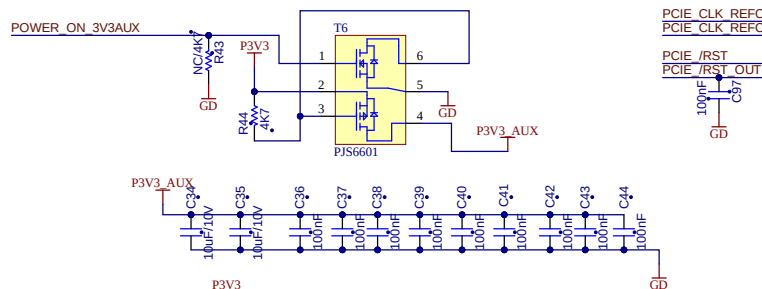
Compatible 22p 0.5mm Waveshare

Compatible avec HDMI Adapter Horizontal (waveshare)

Compatible 15p 1mm Waveshare



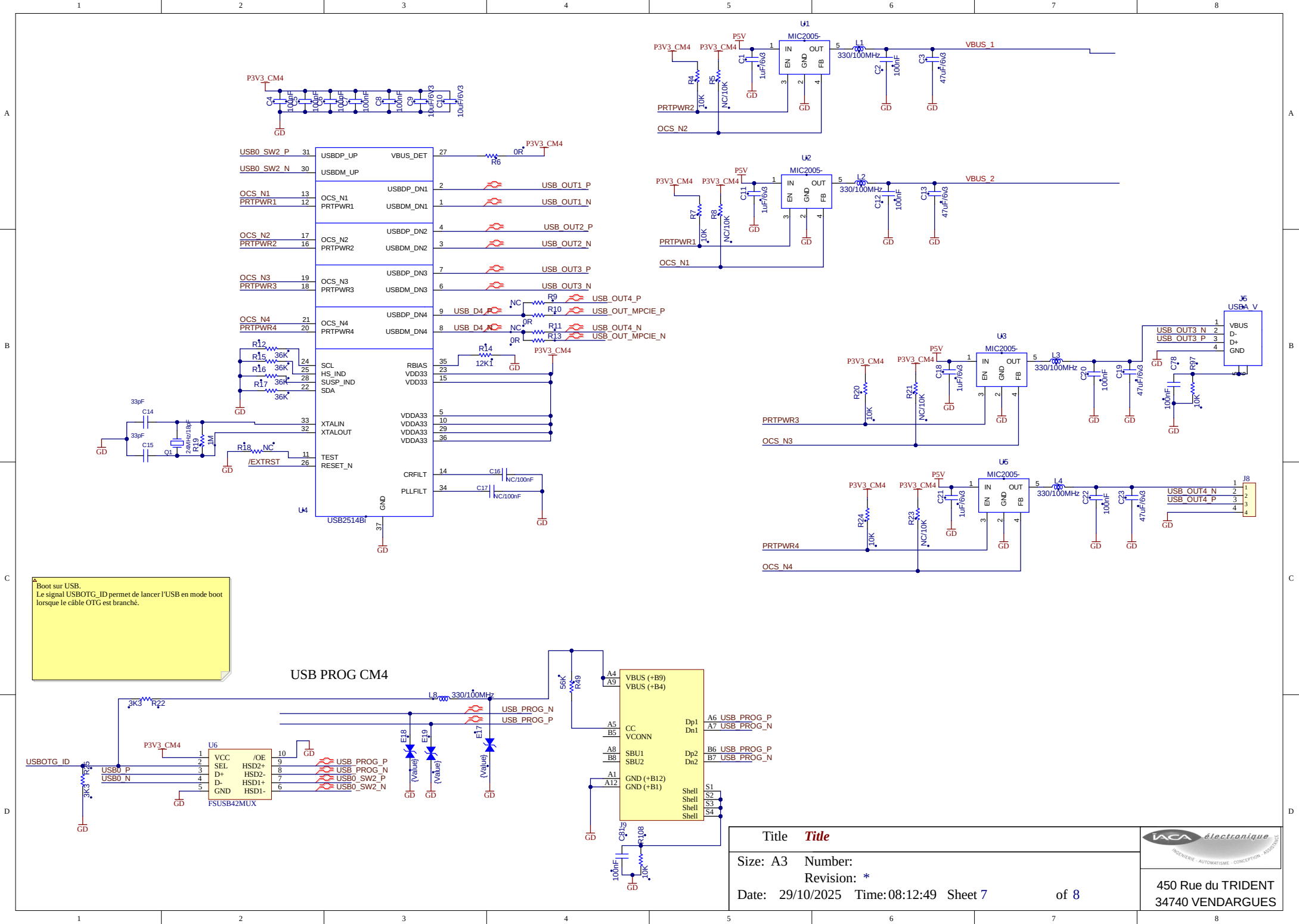
ON 3V3 AUX FROM RA2



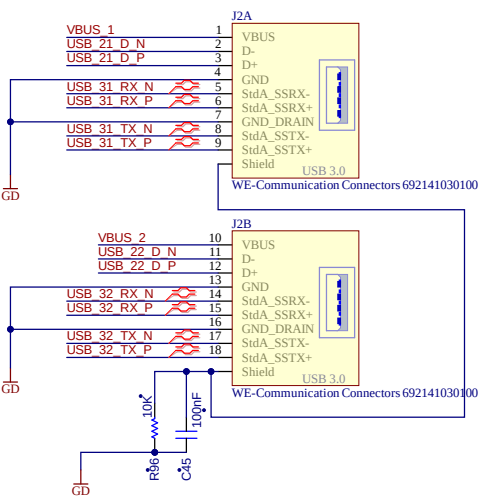
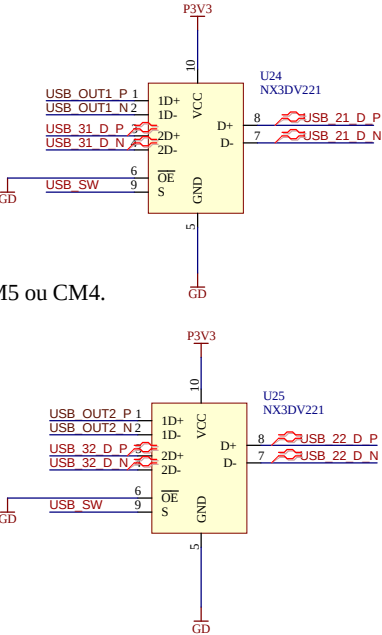
Correction Bug start CM5 avec HDMI

Modification du pinout pour le Nvme Passage sur PCIe 0

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USB_SW pour validation USB2.0 sur CM5 ou CM4.
USB_SW = 0 --> USB_2.0 CM4
USB_SW = 1 --> USB_2.0 CM5



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